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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/512,267	02/24/2000	Woon-Yong Park	06192.0100	5968

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EXAMINER

KUMAR, SRILAKSHMI K

ART UNIT	PAPER NUMBER
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2675

DATE MAILED: 03/14/2002

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Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/512,267

Applicant(s)

PARK ET AL.

Examiner

Srilakshmi K. Kumar

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-7 and 16 is/are rejected.
- 7) ☒ Claim(s) 8-15 and 17-20 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

2. Claims 1-6 and 16 rejected under 35 U.S.C. 102(e) as being anticipated by Kim et al. (US 6,229,516).

As to independent claim 1, Kim et al disclose a liquid crystal display, comprising;

a first gate line block including a plurality of first gate lines transmitting scanning signals, said first gate line block scanning in a first direction (Fig. 2, items 22 upper gate, G1 to Gm);

a second gate line block including a plurality of second gate lines transmitting scanning signals, said second gate line block scanning in a second direction (Fig. 2, items 24 lower gate, Gm+1 to G2m);

a plurality of first data lines transmitting image signals and crossing the first gate lines of said first gate line block (Fig. 2, item 12);

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a plurality of second data lines transmitting image signals and crossing the second gate lines of said second gate line block (Fig. 2, item 14);

a plurality of pixels configured in a matrix pattern and defined by the gate lines and the data lines, said pixels including switching elements coupled to the gate lines and the data lines (Fig. 2, col. 4, lines 15-29);

wherein the first direction is opposite to the second direction and the first data lines are separated from the second data lines (Fig. 2).

As to independent claim 4, limitations of claim 1, and further comprising, a first frame memory (Fig. 2, item 42) that receives and writes external image signals in synchronization with the write clock signals and outputs the image signals to the first data driver in synchronization with the read clock signals (Fig. 2, and col. 4, lines 30-36, col. 7, lines 22-47);

a second frame memory that receives and writes external image signals in synchronization with the write clock signals and outputs the image signals to the second data driver in synchronization with the read clock signals (Fig. 2, and col. 4, lines 30-36, col. 7, lines 22-47);.

As to independent claim 16, see limitations of claims 1 and 4.

As to dependent claims 2 and 5, limitations of claims 1 and 4, respectively, and further comprising wherein the number of the first gate lines is equal (Fig. 1, items G1 to Gm, Gm+1 to G2m, and col. 4, lines 8-10).

As to dependent claims 3 and 6, limitations of claims 2 and 5, respectively, and further comprising wherein the first gate lines and the second gate lines are simultaneously scanned, col. 7, lines 34-44).

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3. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kim et al. (US 6,229,516) as applied to claims 1, 4 and 16 above, and further in view of Tanioka et al. (US 5,093, 655).

As to dependent claim 7, limitations of claim 5, and further comprising wherein polarities of the data voltages supplied to the pixels coupled to adjacent gate lines of said first gate line block are opposite to each other with respect to the common voltage, and the polarities of the data voltages supplied to the pixels coupled to the neighboring gate lines of said second gate line block are opposite to each other with respect to the common voltage. Kim et al do not disclose the polarities.

Tanioka et al disclose in Fig. 1, items 60 and 70 and col. 4, lines 13-26, wherein the polarities of the data voltages supplied to the pixels coupled to adjacent gate lines are opposite to each other. It would have been obvious to one of ordinary skill in the art to incorporate the polarity system of Tanioka et al into that of Kim et al. To reverse polarity is advantageous as it reduces flickers of the entire picture face as disclosed by Tanioka et al in col. 2, lines 43-49.

#### *Allowable Subject Matter*

4. Claims 8-15 and 17-20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

As to claim 8, the prior art of record Kim et al and Tanioka et al fail to disclose where the first gate driver sequentially supplies the scanning signals to gate lines *from the last gate line to the first gate line of the first gate line block*, and said second gate driver sequentially supplies the

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scanning lines to the gate lines *from the first gate line to the last gate line of said second gate line block*.

As to claim 12, the prior art of record Kim et al and Tanioka et al fail to disclose where the first gate driver sequentially supplies the scanning signals to the gate lines *from the first gate line to the last gate line of said first gate line block*, and the second gate driver sequentially supplies the scanning signals to the gate lines *from the second gate line to the last gate line of said second gate line block*.

Claims 9-11, 13-15, 17-20 are objected to as they depend upon a rejected claim.

### ***Conclusion***

**Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D.C. 20231

**Or faxed to:**

(703) 308-9051, (for formal communications intended for entry)

**Or:**

(703) 308-6606 (for informal or draft communications, please label

“PROPOSED” or DRAFT”)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal drive,

Arlington, VA, Sixth Floor (Receptionist)

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Srilakshmi K. Kumar whose telephone number is 703 306 5575.

The examiner can normally be reached on 8:00 am to 5:30 pm alternate Fridays off.


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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Steven J. Saras can be reached on 703 305 9720. The fax phone numbers for the organization where this application or proceeding is assigned are 703 306-0377 for regular communications and 703 308 9051 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703 305 4700.

Srilakshmi K. Kumar  
Examiner  
Art Unit 2675

SKK  
March 7, 2002



STEVEN SARAS  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600